

PTC thermistors

Series/Type: C1098, C1084, C1172

The following products presented in this data sheet are being withdrawn.

Ordering Code		Deadline Last Orders	Last Shipments
B59172C1130A151	2019-03-01	2019-06-07	2019-09-07
B59098C1100B051	2019-03-01	2019-06-07	2019-09-07
B59084C1080B140	2019-03-01	2019-06-07	2019-09-07



Ordering Code	Substitute Product	Date of Withdrawal	Deadline Last Orders	Last Shipments
B59173C1130A151		2021-02-19	2021-05-28	2021-08-27
B59184C1120B153		2021-02-19	2021-05-28	2021-08-27
B59183C1160A140		2021-02-19	2021-05-28	2021-08-27

Please contact your nearest TDK sales office if you need support in selecting a suitable substitute. The addresses of our worldwide sales network are presented at www.tdk-electronics.tdk.com/sales.



Leaded disks

Applications

- Overcurrent protection for telecom applications
- Suitable for line card applications e.g. POTS, access networks, customer premises equipment (CPE) or integrated voice data (IVD)

Features

- Compliant with ITU-T standards
 - basic-level lightning surges (10/700 μs)
 - basic-level power induction (600 V, 1 A, 0.2 s)
 - power contact criteria A/B (230 V, 15 min.)
- Matching available with narrow resistance tolerance
- Tight resistance matching maintained after switching
- Negligible resistance drift after soldering or switching
- Marking: Type, manufacturer's logo, reference temperature in °C and date code YYWW
- UL approval to UL 1434 (file number E69802) for B59154U1135B140, B59184C1120B153 and B59184C1130A151
- RoHS-compatible

Options

Alternative tolerances and resistances on request

Delivery mode

- Taped on reel: Cardboard tape, 360-mm reel, taping to IEC 60286-2
- Bulk: In plastic bags

General technical data

Maximum fault voltage ¹⁾		$V_{F,max}$	245	V AC
Max. operating voltage		V _{max}	135	V AC
Operating temperature range	(V = 0)	T _{op}	-20/+125	°C
Operating temperature range	$(V = V_{max})$	T _{op}	0/+70	°C

1) The maximum fault voltage V_{F,max} is the highest voltage that is permitted to be applied across the PTC thermistor in protection mode.



Leaded disks

Туре	R _R	ΔR_R	R _{25,match}	I _R	I _R	l _s	I _{Smax}	Ordering code
			(per packing unit)	@	@	@		-
			$ \mathbf{R}_1 - \mathbf{R}_2 _{max}$	25°C	70°C	25°C		
	Ω	%	Ω	mA	mA	mA	Α	
C1098	6	±17	No	185	110	440	3.0	B59098C1100B051
C1084	20.8	±20	1	80	40	240	3.0	B59084C1080B140
U1154	50	±15	1	65	45	150	2.5	B59154U1135B140
C1172	50	±15	3	95	63	250	2.0	B59172C1130A151
C1184	50	±15	1.0	90	60	190	2.5	B59184C1120B153
C1184	50	±15	1.0	100	60	210	2.5	B59184C1130A151
C1173	55	±15	3.0	90	60	210	2.5	B59173C1130A151
C1183	55	±15	3	115	90	270	3.0	B59183C1160A140

Electrical specifications and ordering codes

Switching times and ordering codes

Туре	R _R	t _s (typ.) @ I _{Smax}	t _s (typ.) @ 1 A	t _s (typ.) @ 500 mA	Ordering code
	Ω	S ISmax	S	s soo ma	
C1098	6	1.5	14.0	70.0	B59098C1100B051
C1084	20.8	0.06	0.5	2.0	B59084C1080B140
U1154	50	0.05	0.3	1.1	B59154U1135B140
C1172	50	0.25	1.7	6.0	B59172C1130A151
C1184	50	0.06	0.8	3.1	B59184C1120B153
C1184	50	0.08	1.1	4.0	B59184C1130A151
C1173	55	0.09	1.3	5.0	B59173C1130A151
C1183	55	0.25	2.3	9.0	B59183C1160A140



Leaded disks

Dimensional drawings 1)



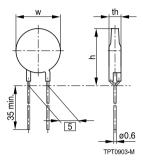
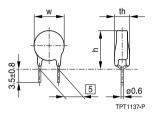


Figure 2



1) The lead length stated in the dimensional drawing refers to the untaped version. For dimensions of the taped version, see chapter "Taping and packing".

Туре	R _R	W _{max}	h _{max}	th _{max}	Figure	Packaging	Ordering code
	Ω	mm	mm	mm			
C1098	6	13.0	17.0	5.0	Figure 1	Taped on reel	B59098C1100B051
C1084	20.8	7.5	4.0	10.5	Figure 2	Bulk	B59084C1080B140
U1154	50	6.0	10.0	4.0	Figure 2	Bulk	B59154U1135B140
C1172	50	8.0	11.0	5.5	Figure 1	Taped on reel	B59172C1130A151
C1184	50	9.0	12.5	4.5	Figure 1	Taped on reel	B59184C1120B153
C1184	50	9.0	13.0	4.5	Figure 1	Taped on reel	B59184C1130A151
C1173	55	8.0	11.0	5.5	Figure 1	Taped on reel	B59173C1130A151
C1183	55	9.5	13.0	6.0	Figure 2	Bulk	B59183C1160A140



Leaded disks

Reliability data

Test	Standard	Test conditions	$ \Delta R_{25}/R_{25} $
Electrical endurance,	IEC 60738-1	Room temperature, I _{Smax} ; V _{max}	< 20%
cycling		Number of cycles: 10	
Electrical endurance,	IEC 60738-1	Storage at V _{max} and T _{op,max} (@ V _{max})	< 25%
constant		Test duration: 1000 h	
Damp heat	IEC 60738-1	Temperature of air: 40 °C	< 10%
		Relative humidity of air: 93%	
		Duration: 56 days	
		Test according to IEC 60068-2-78	
Rapid change	IEC 60738-1	$T_1 = T_{op,min} (0 V), T_2 = T_{op,max} (0 V)$	< 10%
of temperature		Number of cycles: 5	
		Test duration: 30 min	
		Test according to IEC 60068-2-14, test Na	
Vibration	IEC 60738-1	Frequency range: 10 to 55 Hz	< 5%
		Displacement amplitude: 0.75 mm	
		Test duration: 3×2 h	
		Test according to IEC 60068-2-6, test Fc	
Shock	IEC 60738-1	Acceleration: 400 m/s ²	< 5%
		Pulse duration: 6 ms; 6×5000 pulses	
Climatic sequence	IEC 60738-1	Dry heat: $T = T_{op,max} (0 V)$	< 10%
		Test duration: 16 h	
		Damp heat first cycle	
		Cold: $T = T_{op,min} (0 V)$	
		Test duration: 2 h	
		Damp heat 5 cycles	
		Tests performed according to	
		IEC 60068-2-30	



Leaded disks

ITU performance overview and fault conditions

	Test	ITU K20		ITU K21		ITU K45	
	no.	Basic test level	Enhanced test level	Basic test level	Enhanced test level	Basic test level	Enhanced test level
Power induction	1	А	Α	Α	Α	Α	А
	2	В	С	В	С	В	С
Power contact	3	D	E	D	E	D	E
Lightning surge	4	F	G	G	G	G	G
	5	Н	Н	Н	I	Н	Н

	А	600 V AC, R = 600 Ω , t = 0.2 s, criteria A
Power induction	В	600 V AC, R = 600 Ω , t = 1.0 s, with GDT, criteria A
	С	1500 V AC, R = 200 Ω , t = 2.0 s, with GDT, criteria A
	D	230 V AC, t = 15 min, R = 10 1000 Ω, criteria B
Power contact	_	230 V AC, t = 15 min, R = 10, 20, 40, 80, 1000 Ω, criteria B,
	E	R = 160, 300, 600 Ω, criteria A
	F	$V_{c(max)}$ = 1.0 kV, R = 25 Ω , t = 10/700 µs, without GDT, criteria A
Lightning surge	G	$V_{c(max)}$ = 1.5 kV, R = 25 Ω , t = 10/700 µs, without GDT, criteria A
Lightining surge	Н	$V_{c(max)}$ = 4.0 kV, R = 25 Ω , t = 10/700 µs, with GDT, criteria A
	Ι	$V_{c(max)}$ = 6.0 kV, R = 25 $\Omega,$ t = 10/700 $\mu s,$ with GDT, criteria A

Note: Use a GDT (gas discharge tube) with adequate electrical properties in order to ensure reliable operation at enhanced test levels (power induction, lightning surge).

Criteria A: no damage, function must be fulfilled. Criteria B: no fire hazard.



Leaded disks

Additional tests and test conditions

For type C1172:

Enhanced test condition to K.20 power induction	600 V _{AC} , R = 600 Ω , t = 0.5 s, without GDT, criteria A (no damage, function must be fulfilled)
Enhanced test condition to K.20 single port, lightning	$V_{c(max)} = 2500 \text{ V}, t = 1.2/50 \mu\text{s}, R = 2 \Omega$, without GDT, criteria A (no damage, function must be fulfilled) Number of cycles: 500

For type C1173:

Enhanced test condition to K.20	600 V _{AC} , R = 600 Ω , t = 0.5 s, without GDT, criteria A
power induction	(no damage, function must be fulfilled)
Enhanced test condition to K.20	$V_{c(max)} = 4 \text{ kV}, t = 10/700 \mu\text{s}, R = 40 \Omega$, without GDT,
single port, lightning	criteria A (no damage, function must be fulfilled)

For type C1183:

GR1089 Second level intra building AC power fault	120 V_{AC} (50 Hz), short circuit current 25 A, t = 15 min, criteria A (no damage, function must be fulfilled)
Enhanced test condition to K.20	600 V _{AC} , R = 600 Ω, t = 1 s, without GDT, criteria A
power induction	(no damage, function must be fulfilled)
Enhanced test condition to K.20	$V_{c(max)}$ = 4 kV, t = 10/700 µs, R = 40 Ω, without GDT,
single port, lightning	criteria A (no damage, function must be fulfilled)

For type C1184:

Enhanced test condition to K.20	600 V _{AC} , R = 600 Ω , t = 0.5 s, without GDT, criteria A
power induction	(no damage, function must be fulfilled)
Enhanced test condition to K.20	230 V_{AC} , t = 15 min., R = 10 1000 Ω, criteria A
power contact	(no damage, function must be fulfilled)

For type U1154:

GR1089 Second level intra building AC power fault	120 V_{AC} (50 Hz), short circuit current 25 A, t = 15 min, criteria A (no damage, function must be fulfilled)
Enhanced test condition to GR1089 first level intra building lightning surge for multi-pair ports	$ \begin{split} V_{\text{c(max)}} &= \pm 2.25 \text{ kV} \text{ (each polarity 1 application), t} = 2/10 \mu\text{s}, \\ R_{\text{G}} &= 2 \Omega, \text{ R} = 12 \Omega, \text{ criteria A} \\ \text{(no damage, function must be fulfilled)} \end{split} $



Leaded disks

Cautions and warnings

General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

Storage

- Store thermistors only in original packaging. Do not open the package prior to processing.
- Storage conditions in original packaging: storage temperature -25 °C ... +45 °C, relative humidity ≤75% annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within the following period after delivery:
 - Through-hole devices (housed and leaded PTCs): 24 months
 - Motor protection sensors, glass-encapsulated sensors and probe assemblies: 24 months
 - Telecom pair and quattro protectors (TPP, TQP): 24 months
 - Leadless PTC thermistors for pressure contacting: 12 months
 - Leadless PTC thermistors for soldering: 6 months
 - SMDs in EIA sizes 3225 and 4032, and for PTCs with metal tags: 24 months
 - SMDs in EIA sizes 1210 and smaller: 12 months

Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- The ceramic and metallization of the components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering (where applicable)

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.
- Standard PTC heaters are not suitable for soldering.



Leaded disks

Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force and pressure of the clamping contacts pressing against the PTC must be 10 N and 50 kPa, respectively. In case the assembly is exposed to mechanical shock and/ or vibration this force should be higher in order to avoid movement of the PTC during operation.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.

Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).

This listing does not claim to be complete, but merely reflects the experience of EPCOS AG.

Display of ordering codes for EPCOS products

The ordering code for one and the same EPCOS product can be represented differently in data sheets, data books, other publications, on the EPCOS website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.epcos.com/orderingcodes



Leaded disks

Symbols and terms

Symbol	Term
A	Area
С	Capacitance
C _{th}	Heat capacity
f	Frequency
I	Current
I _{max}	Maximum current
I _R	Rated current
I _{res}	Residual current
I _{PTC}	PTC current
l _r	Residual currrent
I _{r,oil}	Residual currrent in oil (for level sensors)
$I_{r,air}$	Residual currrent in air (for level sensors)
I _{RMS}	Root-mean-square value of current
ls	Switching current
I _{Smax}	Maximum switching current
LCT	Lower category temperature
Ν	Number (integer)
N _c	Operating cycles at V _{max} , charging of capacitor
N _f	Switching cycles at V _{max} , failure mode
Р	Power
P ₂₅	Maximum power at 25 °C
P _{el}	Electrical power
P_{diss}	Dissipation power
R _G	Generator internal resistance
R_{min}	Minimum resistance
R _R	Rated resistance @ rated temperature T _R
ΔR_{R}	Tolerance of R _B
R _P	Parallel resistance
R _{PTC}	PTC resistance
R _{ref}	Reference resistance
Rs	Series resistance
R ₂₅	Resistance at 25 °C
R _{25,match}	Resistance matching per reel/ packing unit at 25 °C
ΔR_{25}	Tolerance of R ₂₅



Leaded disks

Т	Temperature
t	Time
T _A	Ambient temperature
ta	Thermal threshold time
T _c	Ferroelectric Curie temperature
t _E	Settling time (for level sensors)
T _B	Rated temperature @ 25 °C or otherwise specified in the data sheet
T_{sense}	Sensing temperature
T _{op}	Operating temperature
T _{PTC}	PTC temperature
t _R	Response time
T _{ref}	Reference temperature
T _{Rmin}	Temperature at minimum resistance
ts	Switching time
T _{surf}	Surface temperature
UCT	Upper category temperature
V or V_{el}	Voltage (with subscript only for distinction from volume)
V _{c(max)}	Maximum DC charge voltage of the surge generator
V _{F,max}	Maximum voltage applied at fault conditions in protection mode
V _{RMS}	Root-mean-square value of voltage
V_{BD}	Breakdown voltage
V _{ins}	Insulation test voltage
$V_{\text{link,max}}$	Maximum link voltage
V _{max}	Maximum operating voltage
V _{max,dyn}	Maximum dynamic (short-time) operating voltage
V _{meas}	Measuring voltage
$V_{\text{meas},\text{max}}$	Maximum measuring voltage
V _R	Rated voltage
V _{PTC}	Voltage drop across a PTC thermistor
α	Temperature coefficient
Δ	Tolerance, change
δ_{th}	Dissipation factor
$ au_{th}$	Thermal cooling time constant
λ	Failure rate
e	Lead spacing (in mm)



The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
- 3. The warnings, cautions and product-specific notes must be observed.
- 4. In order to satisfy certain technical requirements, some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous). Useful information on this will be found in our Material Data Sheets on the Internet (www.tdk-electronics.tdk.com/material). Should you have any more detailed questions, please contact our sales offices.
- 5. We constantly strive to improve our products. Consequently, the products described in this publication may change from time to time. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.

We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available. The aforementioned does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.

- 6. Unless otherwise agreed in individual contracts, all orders are subject to our General Terms and Conditions of Supply.
- 7. Our manufacturing sites serving the automotive business apply the IATF 16949 standard. The IATF certifications confirm our compliance with requirements regarding the quality management system in the automotive industry. Referring to customer requirements and customer specific requirements ("CSR") TDK always has and will continue to have the policy of respecting individual agreements. Even if IATF 16949 may appear to support the acceptance of unilateral requirements, we hereby like to emphasize that only requirements mutually agreed upon can and will be implemented in our Quality Management System. For clarification purposes we like to point out that obligations from IATF 16949 shall only become legally binding if individually agreed upon.



Important notes

8. The trade names EPCOS, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, ExoCore, FilterCap, FormFit, LeaXield, MiniBlue, MiniCell, MKD, MKK, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, ThermoFuse, WindCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.

Release 2018-10