

Data and Signal Line Chokes

Common-mode Chokes, EIA 1210

Series/Type: ACT1210

Date: April 2025

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SMD

Rated voltage: 80 V DC Rated inductance: 11 ... 100 µH Rated current: 150 ... 300 mA

Construction

- Current-compensated double choke
- Ferrite I core
- Winding: enamel copper wire
- Winding welded to terminals

Features

- Operating temperature range: -50 ... +150 °C
- Qualified to AEC-Q200
- Suitable for lead-free reflow soldering as referenced in JEDEC J-STD 020D
- RoHS-compatible

Function

Suppression of asymmetrical interference coupled in on lines, whereas data signals up to some MHz can pass unaffectedly.

Applications

CAN bus and FlexRay systems

Terminals

One-sided tinned terminals:

- Base material CuSn8
- Electro-plating Sn with Ni underlayer
- Lead-free tinned

Marking

Marking on component:

First line

- L value ("A" = 11 μ H, "B" = 22 μ H, "C" = 51 μ H, "D" = 100 μ H)
- Production location "H" = Heidenheim (Germany), "M" = Szombathely (Hungary)
- Last two digits of production order

Second line

Date of manufacture (YWWD)

Delivery mode and packing unit

- 12-mm blister tape, wound on 330-mm Ø reel
- Packing unit: 6000 pcs./reel

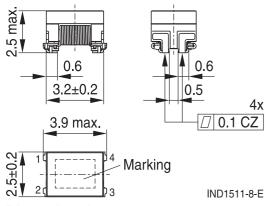




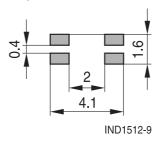
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Dimensional drawing and pin configuration

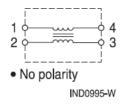


Layout recommendation



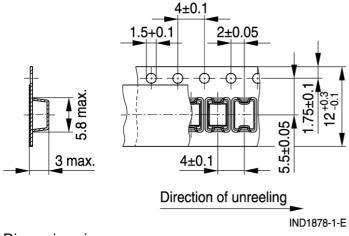
Dimensions in mm

Circuit diagram

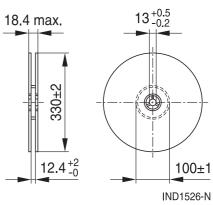


Taping and packing

Blister tape



Reel



Dimensions in mm



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Technical data and measuring conditions

Rated voltage V _R	80 V DC				
Max. component temperature	+150 °C				
Rated current I _R	Referred to 50 Hz and +20 °C				
Rated inductance L _R	Measured with Keysight E4990A (or equivalent) at 100 kHz, 100 mV, +20 °C, inductance is specified in common-mode				
Inductance tolerance	-30/+50% at +20 °C				
Stray inductance L _{stray,typ}	Measured with Keysight E4990A (or equivalent) at 100 kHz, 100 mV, +20 °C, typical values				
DC resistance R _{max}	Measured at +20 °C, specified per winding				
Insulation resistance R _{iso,min}	10 M Ω , measured at 50 V DC				
Rated impedance Z _{min}	Measured at +20 °C, 10 MHz, 100 mV in common-mode				
Rated impedance Z _{typ}	Measured at +20 °C, 10 MHz, 100 mV in common-mode				
Solderability	Dip and look method Sn95.5Ag3.8Cu0.7: +(245 ±5) °C, (3 ±0.3) s				
	Wetting of soldering area ≥ 90% (based on IEC 60068-2-58)				
Resistance to soldering heat	+260 °C, 40 s as referenced in JEDEC J-STD 020D				
Climatic category	55/150/56 (to IEC 60068-1)				
Storage conditions (packaged)	–25 °C +40 °C, ≤ 75% RH				
Weight	Approx. 0.075 g				

Characteristics and ordering codes

L _R	L _{stray,typ}	I _R	R _{max}	Z _{min}	Z _{typ}	Internal code	Ordering code
μΗ	μΗ	mA	Ω	Ω	Ω		
11	0.05	300	0.4	300	550	B82786C0113H002	ACT1210-110-2P-TL00
22	0.06	250	0.5	500	1100	B82786C0223H002	ACT1210-220-2P-TL00
51	0.09	200	0.7	1000	2600	B82786C0513H002	ACT1210-510-2P-TL00
100	0.13	150	1.5	2200	5100	B82786C0104H002	ACT1210-101-2P-TL00

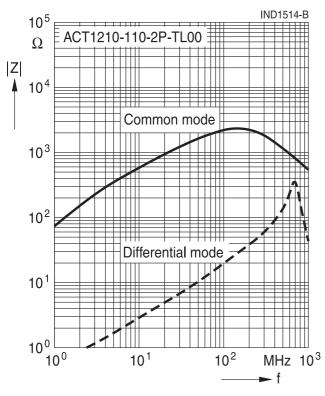


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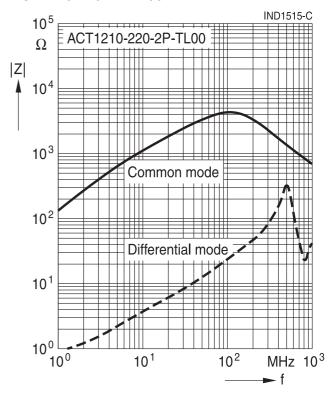
SMD

Impedance versus frequency (typical values)

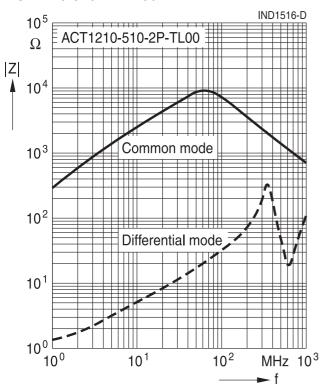
ACT1210-110-2P-TL00



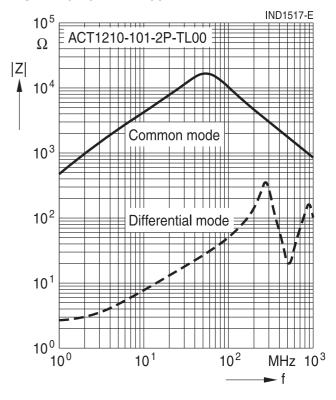
ACT1210-220-2P-TL00



ACT1210-510-2P-TL00



ACT1210-101-2P-TL00

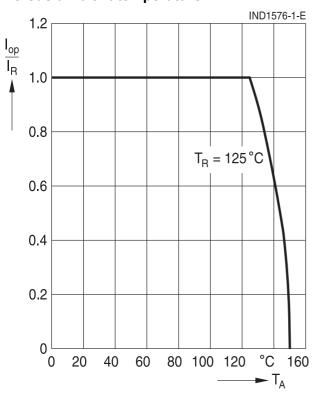




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Current derating I_{op}/I_{R} versus ambient temperature





Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition), online catalogs and in the
 - Particular attention should be paid to the derating curves, if given. Derating applies in the case the ambient temperature in application exceeds the rated temperature of the component.
 - Ensure the operation temperature of the component in application not to exceed the maximum specified value or the upper climatic category temperature.
 - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pins only. Temperatures specified in relation to reflow soldering can also refer to the pins or terminals for products with larger thermal mass, as in such cases, the temperature difference to the top of the component is too big (e.g., high proportion of core within the component).
- If the components are to be washed or varnished it is necessary to check whether the washing or varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. It is possible for washing or varnish agent residues to have a negative effect in the long-term on wire insulation.
 - Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted, sealed, or varnished in customer applications:
 - Many potting, sealing, or varnishing materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
 - It is necessary to check whether the potting, sealing, or varnishing materials used attack or destroy the wire, wire insulation, plastics or glue.
 - The effect of the potting, sealing, or varnishing materials may change the high-frequency behavior of the components.
 - Many coating materials have a negative effect (chemically and mechanically) on the winding wires, insulation materials and connecting points. Customers are always obliged to determine whether and to what extent their coating materials influence the component. Customers are responsible and bear all risk for the use of the coating material. TDK Electronics does not assume any liability for failures of our components that are caused by the coating material.
- Magnetic core materials such as ferrites are sensitive to direct impact. This can cause the core material to flake or lead to breakage of the magnetic core material.
- Any type of tension or pressure on the product may result in damage and affect its functionality and reliability.
 - The products are only to be attached to fixings or mounting holes provided for this purpose in accordance with the data sheet.
 - If additional mechanical forces are applied to the component, e.g., application of gap pads, it is necessary to check whether they attack or destroy any part of the component.
 - It is not permitted for the product specified in the data sheet to assume a mechanical function in the final application.
- Inductance value can drop if external metallic or magnetic parts will be put close to the coil or into the air gap of the coil or core or magnetic material.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.



Cautions and warnings

Display of ordering codes for TDK Electronics products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications, on the company website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.tdk-electronics.tdk.com/orderingcodes.





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Important notes

8 The trade names EPCOS, CarXield, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, FilterCap, FormFit, InsuGate, LeaXield, MediPlas, MiniBlue, MiniCell, MKD, MKK, ModCap, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PiezoBrush, PlasmaBrush, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, SurfIND, ThermoFuse, WindCap, XieldCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdkelectronics.tdk.com/trademarks.

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