Power line chokes

Current-compensated ring core double chokes
250 V AC, 18 A, 1.3 mH

Series/Type: B82726S2183N020
Date: July 2012
Rated voltage 250 V AC
Rated current 18 A
Rated inductance 1.3 mH

Construction
- Current-compensated ring core double choke
- Ferrite core
- Epoxy base plate (UL 94 V-0)
- Choke fixed with PU compound (UL 94 V-0)
- Sector winding
- Clearance ≥ 2.5 mm, creepage distance ≥ 3.0 mm

Features
- Approx. 1% stray inductance for symmetrical interference suppression
- Suitable for wave soldering
- Design complies with EN 60938-2 (VDE 0565-2)
- RoHS-compatible

Applications
- Suppression of common-mode interferences
- Switch-mode applications

Terminals
- Ends of winding wires
- Hot-dip tinned

Marking
Manufacturer, ordering code, rated current, rated voltage, rated inductance, date of manufacture (MM.YY)

Delivery mode
Cardboard box
### Power line chokes

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### Dimensional drawing and pin configuration

![Dimensional drawing and pin configuration](image)

### Technical data and measuring conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated voltage $V_R$</td>
<td>250 V AC (50/60 Hz)</td>
</tr>
<tr>
<td>Test voltage $V_{test}$</td>
<td>1500 V AC / 2100 V DC, 2 s (line/line)</td>
</tr>
<tr>
<td>Rated temperature $T_R$</td>
<td>$+50 \degree C$</td>
</tr>
<tr>
<td>Rated current $I_R$</td>
<td>Referred to 50 Hz and rated temperature</td>
</tr>
<tr>
<td>Rated inductance $L_R$</td>
<td>Measured with Agilent 4284A at 10 kHz, 0.1 mA, $+20 \degree C$. Inductance is specified per winding.</td>
</tr>
<tr>
<td>Inductance tolerance</td>
<td>$\pm 30%$ at $+20 \degree C$</td>
</tr>
<tr>
<td>Inductance decrease $\Delta L/L_0$</td>
<td>$&lt; 10%$ at DC magnetic bias with $I_R$, $+20 \degree C$</td>
</tr>
<tr>
<td>Stray inductance $L_{stray,typ}$</td>
<td>Measured with Agilent 4284A at 10 kHz, 5 mA, $+20 \degree C$, typical value</td>
</tr>
<tr>
<td>DC resistance $R_{typ}$</td>
<td>Measured at $+20 \degree C$, typical value, specified per winding</td>
</tr>
<tr>
<td>Solderability (lead-free)</td>
<td>Sn96.5Ag3.0Cu0.5: +(245 ±5) °C, (3 ±0.3) s, Wetting of soldering area $\geq 95%$ (to IEC 60068-2-20, test Ta)</td>
</tr>
<tr>
<td>Resistance to soldering heat</td>
<td>+(260 ±5) °C, (10 ±1) s (to IEC 60068-2-20, test Tb)</td>
</tr>
<tr>
<td>(wave soldering)</td>
<td></td>
</tr>
<tr>
<td>Climatic category</td>
<td>40/125/56 (to IEC 60068-1)</td>
</tr>
<tr>
<td>Storage conditions (packaged)</td>
<td>$-25 \degree C$ ... $+40 \degree C$, $\leq 75%$ RH</td>
</tr>
<tr>
<td>Weight</td>
<td>Approx. 85 g</td>
</tr>
</tbody>
</table>
## Characteristics and ordering code

<table>
<thead>
<tr>
<th>$I_R$ (A)</th>
<th>$L_R$ (mH)</th>
<th>$L_{stray,typ}$ ($\mu$H)</th>
<th>$R_{typ}$ (m$\Omega$)</th>
<th>Ordering code</th>
</tr>
</thead>
<tbody>
<tr>
<td>18</td>
<td>1.3</td>
<td>16</td>
<td>5</td>
<td>B82726S2183N020</td>
</tr>
</tbody>
</table>

### Impedance $|Z|$ versus frequency $f$

Measured with windings in parallel at +20 °C, typical value

![Graph of impedance $|Z|$ versus frequency $f$.](IND0782-K)

### Current derating $I_{op}/I_R$ versus ambient temperature $T_A$

Current derating $I_{op}/I_R$ versus ambient temperature $T_A$.

![Graph of current derating $I_{op}/I_R$ versus ambient temperature $T_A$.](IND0802-K-E)

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Please read *Cautions and warnings* and *Important notes* at the end of this document.
Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there. Derating must be applied in case the ambient temperature in the application exceeds the rated temperature of the component.
  - Ensure the operation temperature (which is the sum of the ambient temperature and the temperature rise caused by losses / self-heating) of the component in the application does not exceed the maximum value specified in the climatic category.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.

- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation. Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.

- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.

- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.

- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.
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