Power line chokes

Current-compensated ring core double chokes
250 V AC, 1.4 … 7.8 mH, 6 … 13 A, +60 °C / +70 °C

Series/Type: B82725S2*
Date: March 2017
Rated voltage 250 V AC
Rated inductance 1.4 ... 7.8 mH
Rated current 6 ... 13 A, +60 °C, +70 °C

Construction
- Current-compensated ring core double choke
- Ferrite core with epoxy coating (UL 94 V-0)
- Plastic base plate (UL 94 V-0)
- Plastic spacer (UL 94 V-0, CTI 600)
- Coil fixed on base plate with tape
- Sector winding
- Clearance ≥ 2.5 mm, creepage distance ≥ 3 mm

Features
- High resonance frequency
- Approx. 0.5 ... 1% stray inductance for symmetrical interference suppression
- Suitable for wave soldering
- Design complies with EN 60938-2 (VDE 0565-2) and UL 1283
- B82725S2133N040: UL 1446 class 155(F) electrical insulation system
- RoHS-compatible

Applications
- Suppression of common-mode interferences
- Switch-mode power applications
- Frequency converters

Terminals
- Ends of winding wires
- Hot-dip tinned

Marking
- Product brand, electrical insulation system designation (only B82725S2133N040), ordering code, graphic symbol, rated current, rated voltage, rated inductance, date of manufacture (YYWWD.internal ID code), production place identification code

Delivery mode
- Blister tray in cardboard box
Power line chokes B82725S2*

Current-compensated ring core double chokes

Dimensional drawing and pin configuration

![Dimensional drawing and pin configuration](image)

Part tolerances to ISO 2768-cl / ISO 8015.
Size ISO 14405 (E)
All dimensions in mm

Technical data and measuring conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated voltage $V_R$</td>
<td>250 V AC (50/60 Hz)</td>
</tr>
<tr>
<td>Test voltage $V_{test}$</td>
<td>1500 V AC / 2100 V DC, 2 s (line/line)</td>
</tr>
<tr>
<td>Rated temperature $T_R$</td>
<td>$+60 ^\circ C / +70 ^\circ C$</td>
</tr>
<tr>
<td>Rated current $I_R$</td>
<td>Referred to 50 Hz and rated temperature</td>
</tr>
<tr>
<td>Rated inductance $L_R$</td>
<td>Measured with Agilent 4284A at 10 kHz, 0.1 mA, $+20 ^\circ C$ Inductance is specified per winding.</td>
</tr>
<tr>
<td>Inductance tolerance</td>
<td>$\pm 30%$ at $+20 ^\circ C$</td>
</tr>
<tr>
<td>Inductance decrease $\Delta L/L_D$</td>
<td>$&lt; 10%$ at DC magnetic bias with $I_R$, $+20 ^\circ C$</td>
</tr>
<tr>
<td>Stray inductance $L_{stray,typ}$</td>
<td>Measured with Agilent 4284A at 10 kHz, 5 mA, $+20 ^\circ C$, typical values</td>
</tr>
<tr>
<td>DC resistance $R_{typ}$</td>
<td>Measured at $+20 ^\circ C$, typical values, specified per winding</td>
</tr>
<tr>
<td>Solderability (lead-free)</td>
<td>Sn96.5Ag3.0Cu0.5: $+(245 \pm 3)^\circ C$, $(3 \pm 0.3)$ s Wetting of soldering area $\geq 95%$ (to IEC 60068-2-20, test Ta)</td>
</tr>
<tr>
<td>Resistance to soldering heat (wave soldering)</td>
<td>$+(260 \pm 5)^\circ C$, $(10 \pm 1)$ s (to IEC 60068-2-20, test Tb)</td>
</tr>
<tr>
<td>Climatic category</td>
<td>40/125/56 (to IEC 60068-1)</td>
</tr>
<tr>
<td>Storage conditions (packaged)</td>
<td>$-25 ^\circ C \ldots +40 ^\circ C$, $\leq 75%$ RH</td>
</tr>
<tr>
<td>Weight</td>
<td>Approx. 52 g</td>
</tr>
<tr>
<td>Approvals</td>
<td>UL 1446 class 155(F) &quot;T-EIS-CF1&quot; (E320370) (only B82725S2133N040)</td>
</tr>
</tbody>
</table>

Please read Caution and warnings and Important notes at the end of this document.
Characteristics and ordering codes

<table>
<thead>
<tr>
<th>I_R (A)</th>
<th>L_R (μH)</th>
<th>L_stray_typ (μH)</th>
<th>R_typ (mΩ)</th>
<th>T_R (°C)</th>
<th>Wire Ø d (mm)</th>
<th>Ordering code</th>
</tr>
</thead>
<tbody>
<tr>
<td>6</td>
<td>7.8</td>
<td>35</td>
<td>24</td>
<td>+60</td>
<td>1.0</td>
<td>B82725S2602N041</td>
</tr>
<tr>
<td>6</td>
<td>3.9</td>
<td>33</td>
<td>24</td>
<td>+60</td>
<td>1.0</td>
<td>B82725S2602N002</td>
</tr>
<tr>
<td>10</td>
<td>3.3</td>
<td>35</td>
<td>13.5</td>
<td>+60</td>
<td>1.32</td>
<td>B82725S2103N004</td>
</tr>
<tr>
<td>10</td>
<td>2.8</td>
<td>30</td>
<td>12.5</td>
<td>+60</td>
<td>1.32</td>
<td>B82725S2103N003</td>
</tr>
<tr>
<td>13</td>
<td>1.4</td>
<td>9.6</td>
<td>5.3</td>
<td>+70</td>
<td>1.5</td>
<td>B82725S2133N040</td>
</tr>
</tbody>
</table>

Impedance |Z| versus frequency f
measured with windings in parallel at +20 °C, typical values

Current derating I_{op}/I_R versus ambient temperature T_A

Please read Cautions and warnings and Important notes at the end of this document.
Cautions and warnings

SMD

Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
- Particular attention should be paid to the derating curves given there.
- The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.

If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation. Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.

The following points must be observed if the components are potted in customer applications:
- Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
- It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
- The effect of the potting material can change the high-frequency behaviour of the components.

Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.

Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

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