

Power line chokes

Sine-wave chokes 0.8 ... 2.7 A, 0.5 ... 3.0 mH, +40 °C

Series/Type: B82614

Ordering code:

Date: February 2023

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Sine-wave chokes

Rated voltage 250 V AC Rated current 0.8 ... 2.7 A Nominal inductance 0.5 ... 3.0 mH

Construction

- Single choke
- Air-gapped rectangular ferrite core
- Closed plastic coil former (UL 94 V-0)¹)
- Without encapsulation
- 4-section winding

Features

- High resonance frequency due to 4-section winding
- Low saturation effects due to gapped core
- Suitable for wave soldering
- Design complies with EN 60938-2 (VDE 0565-2)
- Recyclable owing to omission of encapsulation and glue
- RoHS-compatible

Applications

- Suppression of differential-mode interferences
- Switch-mode applications
- Reduction of harmonics and PFC
- SMPS featuring a current pump circuit

Terminals

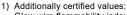
- Base material CuNi18Zn20
- Layer composition Ni, Sn
- Hot-dipped
- \blacksquare Pins 0.7×0.7 (mm)
- Lead spacing 12.5 × 15 (mm)

Marking

■ Product brand (EPCOS), ordering code, rated inductance, rated current, rated voltage, date of manufacture (WWYY)

Delivery mode

Blister tray in cardbox



Glow wire flammability index (GWFI to IEC 60695-2-12): Glow wire ignition temperature (GWIT to IEC 60695-2-13): +775 °C Comparative tracking index (CTI to IEC 60112):

Ball pressure test (BP to IEC 60695-10-2):

+850 °C 175 V +125 °C

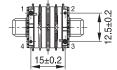


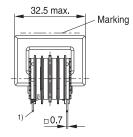


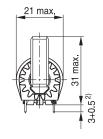
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Dimensional drawing and pin configuration







Tolerances to ISO 2768-cl / ISO 8015. Size ISO 14405 (E) All dimensions in mm +

IND1245-O-E

IND2129-0-E

Circuit diagram



¹⁾ Tin tip permissible 2) Tin tip is not a part of this dimension



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Technical data and measuring conditions

Rated voltage V _R	250 V AC (50/60 Hz)	
Rated temperature T _R	+40 °C	
Rated current I _R	Referred to 50 Hz and rated temperature	
Nominal inductance L _N	Defined at zero DC current bias Measured with Agilent 4284A at 0.1 mA, +20 °C Measuring frequency: $L_R \le 1$ mH = 100 kHz $L_R > 1$ mH = 10 kHz	
Inductance tolerance	±30% at +20 °C	
Inductance at rated current	Measured at DC magnetic bias with I_R with Agilent 4284A at 0.1 mA, +20 °C, typical values Measuring frequency: $L_R \le 1$ mH = 100 kHz $L_R > 1$ mH = 10 kHz	
DC resistance R _{typ}	Measured at +20 °C, typical values	
Solderability (lead free)	Sn96.5Ag3.0Cu0.5: +(245 \pm 5) °C, (3 \pm 0.3) s Wetting of soldering area \geq 95% (to IEC 60068-2-20, test Ta)	
Resistance to soldering heat (wave soldering)	+(260 ±5) °C, (10 ±1) s (to IEC 60068-2-20, test Tb)	
Climatic category	40/125/56 (to IEC 60068-1)	
Storage conditions (packaged)	–25 °C +40 °C, ≤ 75% RH	
Weight	Approx. 30 g	
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Characteristics and ordering codes

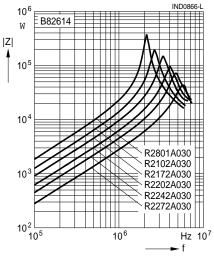
I _R	L _N mH	L at I _R , typ. mH	R_{typ} Ω	Ordering code
0.8	3.0	2.9	1.9	B82614R2801A030
1.0	2.0	1.9	1.3	B82614R2102A030
1.7	1.5	0.95	0.61	B82614R2172A030
2.0	1.0	0.75	0.43	B82614R2202A030
2.4	0.75	0.50	0.33	B82614R2242A030
2.7	0.5	0.42	0.23	B82614R2272A030



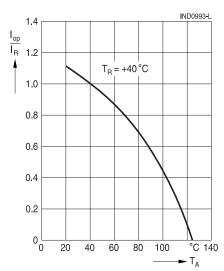
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Impedance |Z| versus frequency f measured at +20 °C, typical values

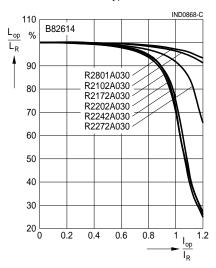


Current derating I_{op}/I_R versus ambient temperature T_A



Relative inductance L_{op}/L_R versus relative current I_{op}/I_R

measured at +20 °C, typical values





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Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
 - Particular attention should be paid to the derating curves given there.
 - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.
 - Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted in customer applications:
 - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
 - It is necessary to check whether the potting material used attacks or destroys the wire, wire insulation, plastics or glue.
 - The effect of the potting material can change the high-frequency behaviour of the components.
 - Many coating materials have a negative effect (chemically and mechanically) on the winding wires, insulation materials and connecting points. Customers are always obligated to determine whether and to what extent their coating materials influence the component. Customers are responsible and bear all risk for the use of the coating material. TDK Electronics does not assume any liability for failures of our components that are caused by the coating material.
- Ceramics / ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

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