

SMT power inductors

Size $8.3 \times 7.5 \times 5.5$ (mm)

Series/Type:

Date:

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B82473A1

SMT power inductors

Size 8.3 x 7.5 x 5.5 (mm)

SMD

Rated inductance 10 ... 470 µH Rated current 0.34 ... 2.3 A

Construction

- Ferrite core
- Winding: enamel copper wire
- Winding soldered to terminals
- Plastic terminal carrier

Features

- Temperature range up to +150 °C
- High rated current
- Low DC resistance
- mended for New Desilor ■ Suitable for lead-free reflow soldering as referenced in JEDEC J-STD 020D
- RoHS-compatible

Applications

- Filtering of supply voltages
- Coupling, decoupling
- DC/DC converters
- Automotive electronics
- Industrial electronics
- Consumer electronics

Terminals

- Base material CuSn6P
- Layer composition Ni, Sn (lead-free)
- Electro-plated

Marking

- Marking on component: L value (μH, coded), manufacturing date (YWWD)
- Minimum data on reel: Manufacturer, ordering code, L value, quantity, date of packing

Delivery mode and packing unit

- 16-mm blister tape, wound on 330-mm Ø reel
- Packing unit: 1000 pcs./reel



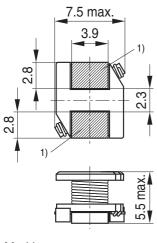


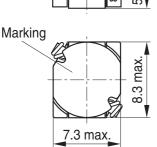
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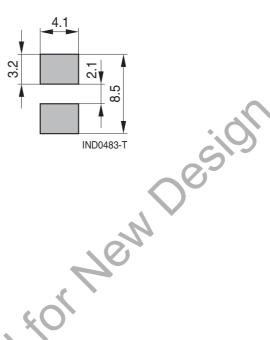
Dimensional drawing and layout recommendation





1) Soldering area

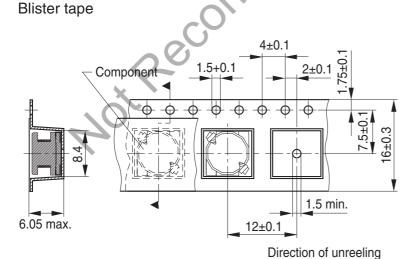
IND0482-K-E



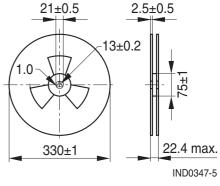
Dimensions in mm

Component tolerances ± 0.2 mm unless otherwise noted.

Taping and packing



Reel



Dimensions in mm

IND0364-H-E



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Technical data and measuring conditions

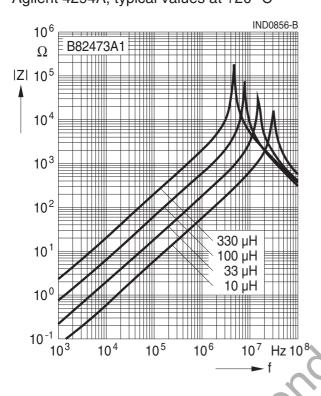
Rated inductance L _R	Measured with LCR meter Agilent 4284A frequency f _L ,			
	0.1 V, +20 °C			
Rated temperature T _R	+85 °C			
Rated current I _R	Max. permissible DC with temperature increase of \leq 40 K at rated temperature			
Saturation current I _{sat}	Max. permissible DC with inductance decrease $\Delta L/L_0$ of approx. 10%			
DC resistance R _{max}	Measured at +20 °C			
Solderability (lead-free)	Dip and look method Sn95.5Ag3.8Cu0.7: $+(245\pm5)$ °C, (5 ± 0.3) s Wetting of soldering area $\geq 90\%$ (based on IEC 60068-2-58)			
Resistance to soldering heat	+260 °C, 40 s (as referenced in JEDEC J-STD 020D)			
Climatic category	55/150/56 (to IEC 60068-1)			
Storage conditions	Mounted: −55 °C +150 °C Packaged: −25 °C +40 °C, ≤ 75% RH			
Weight	Approx. 1.5 g			

weight			Approx. 1.5 g					
Characteristics and ordering codes								
L _R	Tolerance	fL	I _R	I _{sat}	R _{max}	Ordering code		
μΗ		MHz	Α	Α	Ω			
10	±10% ≙ K	0.1	2.30	2.50	0.07	B82473A1103K000		
15		0.1	1.80	2.00	0.09	B82473A1153K000		
22	0	0.1	1.50	1.60	0.11	B82473A1223K000		
33	X	0.1	1.20	1.30	0.13	B82473A1333K000		
47	10,	0.1	1.10	1.20	0.18	B82473A1473K000		
68		0.1	0.85	0.90	0.28	B82473A1683K000		
100		0.1	0.72	0.80	0.43	B82473A1104K000		
150		0.1	0.58	0.65	0.64	B82473A1154K000		
220		0.1	0.49	0.55	0.96	B82473A1224K000		
330		0.1	0.40	0.45	1.26	B82473A1334K000		
470		0.1	0.34	0.40	1.96	B82473A1474K000		



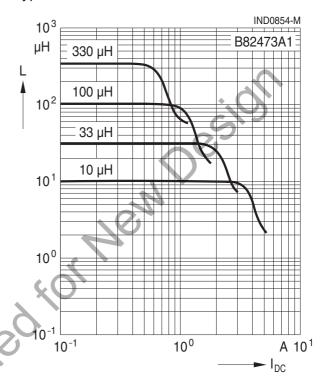
Size 8.3 x 7.5 x 5.5 (mm)

Impedance IZI versus frequency f measured with impedance analyzer Agilent 4294A, typical values at +20 °C

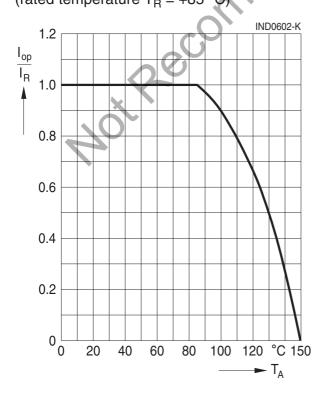


SMD

Inductance L versus DC load current I_{DC} measured with LCR meter, Agilent 4275A, typical values at +20 °C



Current derating I_{op}/I_R versus ambient temperature T (rated temperature $T_R = +85 \, ^{\circ}\text{C}$)





Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition), online catalogs and in the
 - Particular attention should be paid to the derating curves, if given. Derating applies in the case the ambient temperature in application exceeds the rated temperature of the component.
 - Ensure the operation temperature of the component in application not to exceed the maximum specified value or the upper climatic category temperature.
 - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pins only. Temperatures specified in relation to reflow soldering can also refer to the pins or terminals for products with larger thermal mass, as in such cases, the temperature difference to the top of the component is too big (e.g., high proportion of core within the component).
- If the components are to be washed or varnished it is necessary to check whether the washing or varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. It is possible for washing or varnish agent residues to have a negative effect in the long-term on wire insulation.
 - Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted, sealed, or varnished in customer applications:
 - Many potting, sealing, or varnishing materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
 - It is necessary to check whether the potting, sealing, or varnishing materials used attack or destroy the wire, wire insulation, plastics or glue.
 - The effect of the potting, sealing, or varnishing materials may change the high-frequency behavior of the components.
 - Many coating materials have a negative effect (chemically and mechanically) on the winding wires, insulation materials and connecting points. Customers are always obliged to determine whether and to what extent their coating materials influence the component. Customers are responsible and bear all risk for the use of the coating material. TDK Electronics does not assume any liability for failures of our components that are caused by the coating material.
- Magnetic core materials such as ferrites are sensitive to direct impact. This can cause the core material to flake or lead to breakage of the magnetic core material.
- Any type of tension or pressure on the product may result in damage and affect its functionality and reliability.
 - The products are only to be attached to fixings or mounting holes provided for this purpose in accordance with the data sheet.
 - If additional mechanical forces are applied to the component, e.g., application of gap pads, it is necessary to check whether they attack or destroy any part of the component.
 - It is not permitted for the product specified in the data sheet to assume a mechanical function in the final application.
- Inductance value can drop if external metallic or magnetic parts will be put close to the coil or into the air gap of the coil or core or magnetic material.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.



Cautions and warnings

Display of ordering codes for TDK Electronics products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications, on the company website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.tdk-electronics.tdk.com/orderingcodes.



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- 3 The warnings, cautions and product-specific notes must be observed.
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- 6 Unless otherwise agreed in individual contracts, all orders are subject to our General Terms and Conditions of Supply.
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Important notes

8 The trade names EPCOS, CarXield, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, FilterCap, FormFit, InsuGate, LeaXield, MediPlas, MiniBlue, MiniCell, MKD, MKK, ModCap, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PiezoBrush, PlasmaBrush, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, SurfIND, ThermoFuse, WindCap, XieldCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdkelectronics.tdk.com/trademarks.

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