

# Surge arrester

2-electrode arrester

Series/Type: Ordering code: S30-A150X

B88069X6071T203

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# **Product description**

The S30 series has been especially designed to meet data transmission protection requirements. The optimized design features a high level of protection against fast rising transients usually caused by lightning disturbances. For use in high frequency data lines, the series offers ultra low capacitances and shows only marginally signal losses up to high frequencies. The devices are extremely reliable and are able to withstand high surge currents without destruction.

#### **Features**

- Very small size (EIA 1812)
- Short response time
- High current capability
- Stable performance over service life
- Ultra low capacitance and insertion loss
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

# **Applications**

#### **Telecommunication:**

- Ethernet, PoE, xDSL
- Cable modem, splitters, line cards
- Wireless antenna protection

#### Others:

- CCTV
- Switching power supply

#### **Product characteristics**

Physical dimensions (length × width × height)	0.18 × 0.12 × 0.10 in			
	4.5 × 3.2 × 2.7 mm			
	EIA 1812 / 4532 metric			
Weight	~ 0.2	g		
Operating temperature	−40 +125 °C			
Recommended storage <sup>1)</sup> - temperature - humidity - period	+5 +35 45 80 ≤ 2	°C % years		
Climatic category (IEC 60068-1)	40/125/21	40/125/21		
Moisture sensitivity level 2)	1	1		
Marking, black positive				
Certifications	UL 497B (E163070)	UL 497B (E163070)		

#### Notes

2) Tests according to JEDEC J-STD-020

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<sup>1)</sup> Specified in terms of corrosion against Sn-plating



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# Electrical specifications and stress test methods

Nominal DC spark-over voltage 3) 4)		150	V
Tolerance		±30	%
Min.		105	V
Max.		195	V
Impulse spark-over voltage			
at 100 V/µs - for 99% of mea	asured values	< 500	V
- typical values	of distribution	< 400	V
at 1 kV/µs - for 99% of mea	asured values	< 600	V
· - typical values	of distribution	< 500	V
Service life 5)			
10 operations	50 Hz, 1 s	2.5	Α
300 operations	8/20 µs	100	Α
10 operations [5× (+) & 5× (-)]	8/20 µs	3	kA
1 operation	8/20 µs	3.5	kA
10 operations [5× (+) & 5× (-)]	5/320 µs <sup>6)</sup>	150	Α
100 operations [50× (+) & 50× (-)]	10/1000 µs	10	Α
Insulation resistance at 50 $V_{\text{DC}}$		> 1	$G\Omega$
Capacitance at 1 MHz		< 0.4	pF
Arc voltage at 1 A		~ 8	V
Glow to arc transition current		< 0.2	Α
Glow voltage		~ 50	V

Terms and current waveforms in accordance with ITU-T Rec. K. 12; IEC 61643-21 and IEC 61643-311.

 $<sup>^{3)}</sup>$  At delivery AQL 0.65 level II, DIN ISO 2859  $^{4)}$  In ionized mode  $^{5)}$  Tests according to ITU-T Rec. K. 12 and UL 497B  $^{6)}$  Test generator 6 kV, 10/700 µs, 40  $\Omega$ 

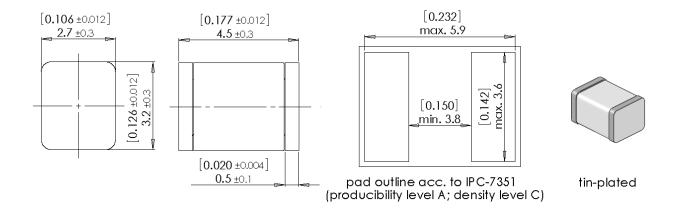


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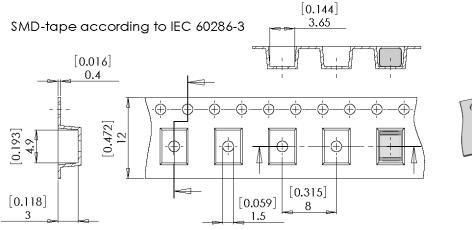
S30-A150X

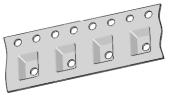
# Dimensions in mm and inch [...]

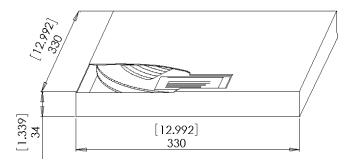


# Ordering code and packing advice

B88069X6071**T203** = 2000 pcs. on SMD-tape & reel







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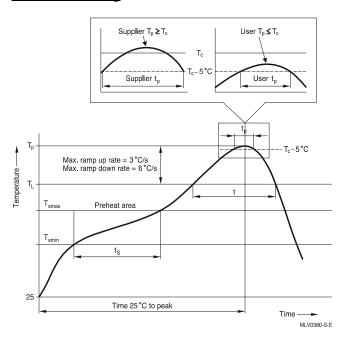


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## Soldering parameter

## Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	T <sub>smin</sub> T <sub>smax</sub> t <sub>smin</sub> to t <sub>smax</sub>	100 °C 150 °C 60 120 s	150 °C 200 °C 60 180 s
Average ramp-up rate	T <sub>smax</sub> to T <sub>p</sub>	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T <sub>L</sub>	183 °C 60 150 s	217 °C 60 150 s
Peak package body temperature *, Classification temperature **	T <sub>p</sub> , T <sub>C</sub>	220 235 °C **	245 260 °C **
Time (t <sub>p</sub> ) ** within 5 °C of the specified classification temperature (T <sub>C</sub> )		20 s ***	30 s ***
Average ramp-down rate	T <sub>p</sub> to T <sub>smax</sub>	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

<sup>\* =</sup> Tolerance for peak profile temperature (T<sub>p</sub>) is defined as a supplier minimum and a user maximum.

Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

#### Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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<sup>\*\* =</sup> For details please refer to JEDEC J-STD-020D.

<sup>\*\* =</sup> Tolerance for time at peak profile temperature (t<sub>p</sub>) is defined as a supplier minimum and a user maximum.



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