

Surge arrester

Stacked surge arresters

Series/Type: LN8A-A800DC-5 Ordering code: B88069X2863T152

Date: 2017-04-11

Version: 05

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B88069X2863T152 Surge arrester

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LN8A-A800DC-5

Features

- Fast response time
- High current rating
- Stable performance over life
- Very low capacitance
- High insulation resistance
- **Excellent SMD handling**
- RoHS-compatible

Applications

DC power supply protection 48 V

Electrical specifications	Arrester only	w. capacitors ²⁾	Unit	
DC spark-over voltage 1)		800 ±25	< 750	V %
Front of wave spark-over voltage at 6 kV, 1.2/50 µs - initial - after service life		< 2000 < 2600	< 900 < 1500	V V
DC operating voltage ³⁾ - nominal - max. (+25%)		48 60		V_{DC}
Service life $\begin{array}{c} 10 \ \ \text{operations} \ [5 \times (+) \ \& \ 5 \times (-)] \\ 10 \ \ \text{operations} \ [5 \times (+) \ \& \ 5 \times (-)] \\ 100 \ \ \text{operations} \ [50 \times (+) \ \& \ 50 \times (-)] \\ 300 \ \ \text{operations} \ (+/- \ \text{alternating polarity}) \\ \hline \text{Insulation resistance at } 100 \ V_{DC} \\ \end{array}$	8/20 μs 10/350 μs 10/350 μs 10/1000 μs	50 10	20 4 20 20 20 10	kA kA A A GΩ
Capacitance at 1 MHz		< 1		pF
Weight		~ 5.5		g
Operation and storage temperature		-40 + 125		°C
Climatic category (IEC 60068-1)		40/125/21		
Marking, red positive		VY 0800 48DC YY - Year of production 0800 - Nominal voltage (arrester only) 48DC - Operating voltage		

¹⁾ At delivery AQL 0.65 level II, DIN ISO 2859 2) Refer to circuit diagram on page 3

Terms in accordance with IEC 61643-11.

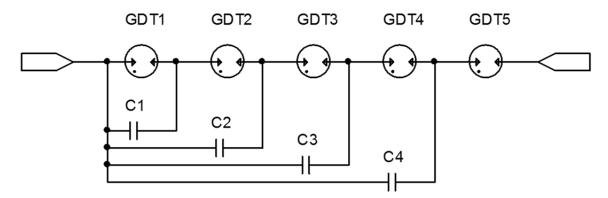
DC current source 30 A



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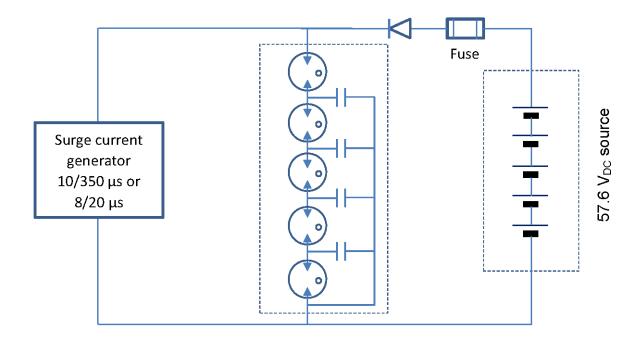
Circuit diagram (C1 to C4 = each capacitor 100 pF to 470 pF)



Recommended capacitor: TDK C4520X7R3D471K130KA

Test circuit

10/350 μs or 8/20 μs circuit



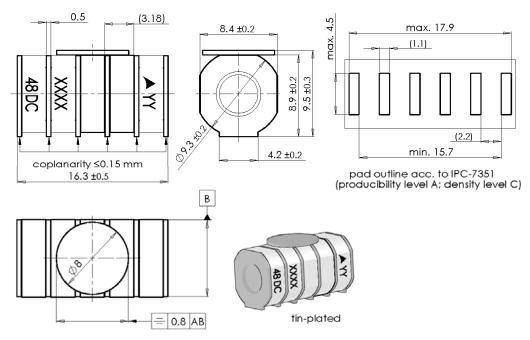
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Dimensional drawing in mm

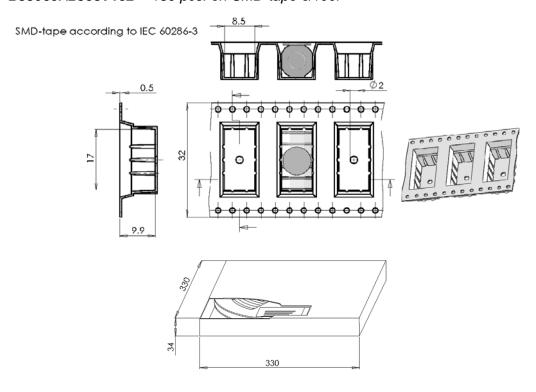


Remark:

 \overline{XXXX} = 0800 (DC spark-over voltage for arrester only)

Ordering code and packing advice

B88069X2863**T152** = 150 pcs. on SMD-tape & reel



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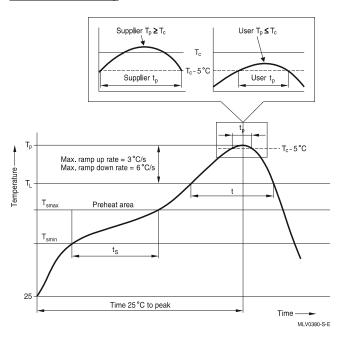


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Soldering parameter

Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	T_{smin} T_{smax} $t_{smin} \text{ to } t_{smax}$	100 °C 150 °C 60 120 s	150 °C 200 °C 60 180 s
Average ramp-up rate	T_{smax} to T_p	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T _L	183 °C 60 150 s	217 °C 60 150 s
Peak package body temperature *, Classification temperature **	T_p, T_C	220 235 °C **	245 260 °C **
Time (t _p) ** within 5 °C of the specified classification temperature (T _C)		20 s ***	30 s ***
Average ramp-down rate	T _p to T _{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

^{*=} Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

Cautions and warnings

- The follow current must be limited (see test circuit) so that the arrester can be properly extinguished when the surge has decayed. The arrester might otherwise heat up and ignite adjacent components.
- If the contacts of the surge arresters are defective, current load can cause sparks and loud noises.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

^{** =} For details please refer to JEDEC J-STD-020D.

^{*** =} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.



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Release 2018-10