Surge arrester

Stacked surge arresters

Series/Type: LN8A-A450DC-2
Ordering code: B88069X1883T302
Date: 2019-07-05
Version: 08
Features
- Fast response time
- High current rating
- Stable performance over life
- Very low capacitance
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

Applications
- DC power supply protection 24 V

### Electrical specifications

<table>
<thead>
<tr>
<th>Feature</th>
<th>Arrester only</th>
<th>w. capacitors</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>DC spark-over voltage ¹)</td>
<td>450 ±30</td>
<td>&lt; 500</td>
<td>V</td>
</tr>
<tr>
<td>Front of wave spark-over voltage at 6 kV, 1.2/50 µs</td>
<td>1100 ±30</td>
<td>&lt; 700</td>
<td>V</td>
</tr>
<tr>
<td>DC operating voltage ³)</td>
<td>24 ±25</td>
<td>25 ±25</td>
<td>VDC</td>
</tr>
</tbody>
</table>

### Service life
- 5 operations 50 Hz, 1 s
- 10 operations [5× (+) & 5× (–)] 5/320 µs ⁴)
- 10 operations [5× (+) & 5× (–)] 8/20 µs
- 2 operations 10/350 µs
- 300 operations (+/– alternating polarity) 10/1000 µs

### Insulation resistance at 100 V<sub>DC</sub>
- > 10 GΩ

### Capacitance at 1 MHz
- < 1 pF

### Weight
- ~ 2.2 g

### Operation and storage temperature
- -40 ... +125 °C

### Climatic category (IEC 60068-1)
- 40/125/21

### Marking, black positive

Certifications
- UL 1449 (E319264)

¹) At delivery AQL 0.65 level II, DIN ISO 2859
²) Refer to circuit diagram on page 3
³) DC current source 30 A
⁴) Test generator 1 kV, 10/700 µs, 40 Ω
Terms in accordance with IEC 61643-11.
Circuit diagram (C1 = 100 pF to 470 pF)

Recommended capacitor: TDK C4520X7R3D471K130KA

Test circuit

10/350 µs or 8/20 µs circuit
Dimensional drawing in mm

Ordering code and packing advice

*B88069X1883T302* = 300 pcs. on SMD-tape & reel

SMD-tape according to IEC 60286-3
Soldering parameter

Reflow soldering

<table>
<thead>
<tr>
<th>Reflow profile features</th>
<th>Sn-Pb eutectic assembly</th>
<th>Pb-free assembly</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preheat and soak</td>
<td>T_{min}</td>
<td>100 °C</td>
</tr>
<tr>
<td>- Temperature min</td>
<td>T_{max}</td>
<td>150 °C</td>
</tr>
<tr>
<td>- Temperature max</td>
<td>T_{max}, t_{min} to T_{max}</td>
<td>60 ... 120 s</td>
</tr>
<tr>
<td>- Time</td>
<td></td>
<td>60 ... 180 s</td>
</tr>
<tr>
<td>Average ramp-up rate</td>
<td>T_{max} to T_{p}</td>
<td>max. 3 °C/ s</td>
</tr>
<tr>
<td>Liquidus temperature</td>
<td>T_{L}</td>
<td>183 °C</td>
</tr>
<tr>
<td>Time at liquidus</td>
<td></td>
<td>217 °C</td>
</tr>
<tr>
<td>Peak package body</td>
<td>T_{p}, T_{C}</td>
<td>220 ... 235 °C</td>
</tr>
<tr>
<td>temperature *,</td>
<td></td>
<td>**</td>
</tr>
<tr>
<td>Classification</td>
<td></td>
<td>245 ... 260 °C</td>
</tr>
<tr>
<td>temperature **</td>
<td></td>
<td>**</td>
</tr>
<tr>
<td>Time (t_{p}) ** within</td>
<td></td>
<td>20 s ***</td>
</tr>
<tr>
<td>5 °C of the specified</td>
<td></td>
<td>30 s ***</td>
</tr>
<tr>
<td>classification</td>
<td></td>
<td>**</td>
</tr>
<tr>
<td>temperature (T_{C})</td>
<td></td>
<td>**</td>
</tr>
<tr>
<td>Average ramp-down rate</td>
<td>T_{p} to T_{max}</td>
<td>max. 6 °C/ s</td>
</tr>
<tr>
<td>Time 25 °C to peak</td>
<td>max. 6 min</td>
<td>max. 8 min</td>
</tr>
</tbody>
</table>

* = Tolerance for peak profile temperature (T_{p}) is defined as a supplier minimum and a user maximum.
** = For details please refer to JEDEC J-STD-020D.
*** = Tolerance for time at peak profile temperature (t_{p}) is defined as a supplier minimum and a user maximum.

Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

Cautions and warnings

- The follow current must be limited (see test circuit) so that the arrester can be properly extinguished when the surge has decayed. The arrester might otherwise heat up and ignite adjacent components.
- If the contacts of the surge arresters are defective, current load can cause sparks and loud noises.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.
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Release 2018-10