

Surge arrester

3-electrode arrester

Series/Type: EZ0-A90XSMD Ordering code: B88069X6051T123

Date: 2021-05-08

Version: 09

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Surge arrester B88069X6051T123

3-electrode arrester EZ0-A90XSMD

Features

- Small size
- Fast response time
- High current rating
- Stable performance over life
- Very low capacitance
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

Applications

- Modem
- Data lines

Electrical specifications

DC spark-over voltage 1) 2) 3)		90	V
Tolerance		±20	%
Min.	72	V	
Max.	108	V	
Impulse spark-over voltage 3)			
at 100 V/µs - for 99% of measured values - typical values of distribution		< 450	V
		< 350	V
at 1 kV/µs - for 99% of measured values - typical values of distribution		< 600	V
		< 500	V
Service life	0		
10 operations	50 Hz; 1 s ⁴⁾	10	Α
1 operation	50 Hz; 0.18 s ⁴⁾	10	Α
10 operations [5× (+) & 5× (–)]	8/20 μs ⁴⁾	10	kA
1 operation	10/350 µs ⁴⁾	1	kA
300 operations (+/-, alternating polarity)	10/1000 μs ⁴⁾	200	Α
Insulation resistance at 50 V _{DC} ³⁾		> 1	$G\Omega$
Capacitance at 1 MHz 3)		< 1.5	pF
Transverse delay time 5)		< 0.2	μs
Arc voltage at 1 A		~ 10	V
Glow to arc transition current		< 0.5	Α
Glow voltage at 0.1 A		~ 60	V
Weight		~ 1	g
Operation and storage temperature		-40 + 125	°C
Climatic category (IEC 60068-1)		40/125/21	
Marking, blue negative		EPCOS EZ 90 YY O EZ - Series 90 - Nominal voltage YY - Year of production O - Non radioactive	

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1) At delivery AQL 0.65 level II, DIN ISO 2859

UL 497B (E163070)



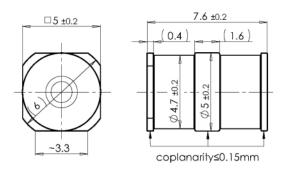
2) In ionized mode

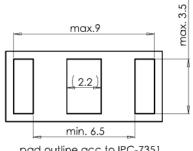
Certifications

- 3) Tip or ring electrode to center electrode
- 4) Total current through center electrode, half value through tip respectively ring electrode.
- 5) Test according to ITU-T Rec. K.12

Terms in accordance with ITU-T Rec. K.12 and IEC 61643-311.

Dimensional drawing in mm







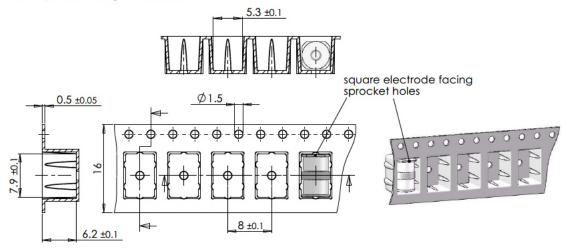
pad outline acc.to IPC-7351 (producibility level A; density level A)

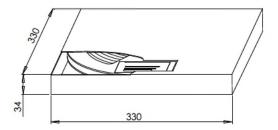
tin-plated

Ordering code and packing advice

B88069X6051**T123** = SMD-tape with 1200 pcs.

SMD-tape according to IEC 60286-3





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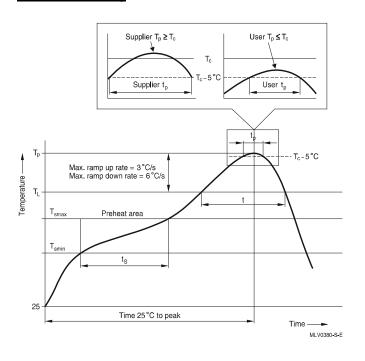


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Soldering parameter

Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time Average ramp-up	Tsmin Tsmax tsmin to tsmax Tsmax to Tp	100 °C 150 °C 60 120 s max. 3 °C/ s	150 °C 200 °C 60 180 s max. 3 °C/ s
Liquidous temperature Time at liquidous	T _L	183 °C 60 150 s	217 °C 60 150 s
Peak package body temperature *, Classification temperature **	T _p , T _C	220 235 °C **	245 260 °C **
Time (t _p) ** within 5 °C of the specified classification temperature (T _C)		20 s ***	30 s ***
Average ramp-down rate	T _p to T _{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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^{** =} For details please refer to JEDEC J-STD-020D.

^{*** =} Tolerance for time at peak profile temperature (t_{p}) is defined as a supplier minimum and a user maximum.



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