

Varistor

Varistor		Material Data Sheet
Product Class:	Housed varistor B722xxT*V87	
Date	30.09.2019	
IMDS ID if available		
Version	5.02	

Product Part (IMDS: semi component)	Material Class (IMDS: Material)	Material (Classification) VDA 231	Substance		TMPS**) [wt%]	CAS if applicable	typical mass of material [wt-%]	Traces see 1)
Active Part	Ceramic	3B	ZnO Bi2O3 Sb2O3 Co3O4 NiO others*)	91 4,0 2,5 1 0,5	1314-13-2 1304-76-3 1309-64-4 1308-06-1 1313-99-1	43,0		
Termination	Composite	4D	Cu or Ag Glass frit (boro-silicat	95 5	7440-50-8 7440-22-4	0,6		
High melting point solder	Heavy Metal	1C8	Sn Ag Cu	96,5 3 0,5	7440-31-5 7440-22-4 7440-50-8	1,2		
Low melting point solder	Heavy Metal	1C8	Sn Bi		42 58	7440-31-5 7440-69-9	0,2	
	Heavy Metal	1A	Fe		100	7439-89-6	5,8	
Leads		1C12	Cu		100	7440-50-8	0,2	
		1C8	Sn		100	7440-31-5	0,2	
Spring	Heavy Metal	1C12	Cu Sn P		92,75 7,0 0,25	7440-50-8 7440-31-5 7723-14-0	3,2	
		1C8	Sn		100	7440-31-5	0,2	
Encapsulation	Duromer	2D Halogen-free coating	SiO2 Epoxy Pyromellitic dianhydride Phosphated epoxy others*)		46 40 6,5 5 2,5	60676-86-0 25036-25-3 89-32-7	7,9	
Plastic housing inner cover	Thermoplastic	2A	Aromatic Liquid Crystal Polymer (LCP) Glass fiber others*)		68 30 2	147310-94-9	12,8	
Plastic housing outer cover	Thermoplastic	2A	Poly butylene terephthalate (PBT) Glass fiber Brominated flame retardant 1 Brominated flame retardant 2 Sb2O3 Other*)		68 16,2 6 6 3 0,8	24968-12-5 65997-17-3 68928-70-1 71342-77-3 1309-64-4	24,7	
•	•			•		Sum in total:	100	
sizes L x W [mm] 21,6 X 17	weight range [g] 3,8-5,5		numbers K130-K420*	sizes L x W [mm] 26 X 22 27,5 X 27,5	weight range [g] 5,8-8.8 9,9-18,0	material numbe B72220T*V87 B72220T*V87	part numbers T20K50-K420* T20K460-K1000*	
Not part of a Pro	oduct Class T2	0K50DF (B72220T050	0K105) and T20K510M	110K4 (B72220T0)511K109) is n	ot a part of this pr	oduct class.	

Umbrella Specification	Rev 5 02 1a	based on ZVFLL avout	TDK Flectronics AG	Dent TO	03/2019



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Contact	Mr. Christoph Ronner		lm	Important remarks:			
Division	PPD Q QM 8530 Deutschlandsberg, AUSTRIA		1)	The declaration limit is 0.1% as defined by IEC 62474 (IEC PAS 61906). Traces are			
Address				product parts, substances etc. that are below a percentage of 0.1 % by weight, if not otherwise regulated.			
	Tel: +43 3462 800 2139	mailto: functional.ppd-eqpm.db@tdk-electror	nics.tdk.com 2)	This Material Data Sheet contains typical values of the respective products set forth			
*) others: .(not declarable or prohibited substances acc. GADSL) **) typical mass percentage of substance				herein. We expressly point out that all values and statements contained herein are based on our best present knowledge and cannot be regarded as binding statemen or binding product specifications, unless otherwise explicitly agreed in writing. TDK ELECTRONICS AG AND ITS AFFILIATES HEREBY EXPRESSLY DISCLAIM ANY REPRESENTATION OR WARRANTY, WHETHER EXPRESS, IMPLIED OR STATUTORY, WITH REGARD TO THE STATEMENTS AND VALUES CONTAINE HEREIN, INCLUDING BUT NOT LIMITED TO ANY REPRESENTATION OR WARRANTY OF MERCHANTABILITY OR SUITABILITY FOR ANY PURPOSE.			
of Directive 201	1/65/EU of the European		th, 2011 on the i	products are compatible with the requirements according to Art. 4 (substance restrictions) restriction of the use of certain hazardous substances in electrical and electronic equipment. valid			
✓ no exemptions □ Exemption 6 (a) □ Exemption 6 (b)): Lead as an alloying elen	ment in steel for machining purposes and in galv		nining up to 0,35 % lead by weight;			
☐ Exemption 6 (c)): Copper alloy containing	up to 4 % lead by weight;					
☐ Exemption 7 (a)): Lead in high melting tem	nperature type solder (i.e. lead-based alloys con	ntaining 85 % by w	eight or more lead);			
☐ Exemption 7 (c)		, ,		ectric ceramic in capacitors, e.g. piezoelectronic devices, or in a glass or ceramic matrix compound;			
☐ Exemption 7 (c))-II: Lead in dielectric ceram	ic in capacitors for a rated voltage of 125 V AC	or 250 V DC or hig	her;			

☐ Exemption 7 (c)-III: Lead in dielectric ceramic in capacitors for a rated voltage of less than 125 V AC or 250 V DC;

☐ Other Exemption than above

🗆 Exemption 15: Lead in solders to complete a viable electrical connection between semiconductor die and carrier within integrated circuit Flip Chip packages;