

Varistor

Material Data Sheet

Product Class	Housed Varistor B722xxT*
Date	30.09.2019
IMDS ID if available	
Version	5.06

Product Part (IMDS: semi component)	Material Class (IMDS: Material)	Material (Classification) VDA 231	Substance		TMPS**) [wt%]	CAS if applicable	typical mass of material [wt-%]	Traces see 1)
Active Part	Ceramic	3B	ZnO Bi2O3 Sb2O3 Co3O4 NiO others*)		91 4,0 2,5 1 0,5	1314-13-2 1304-76-3 1309-64-4 1308-06-1 1313-99-1	18,1	
Termination	Composite	4D	Ag Glass frit (boro-silicate)		95 5	7440-22-4	1,4	
Solder	Heavy Metal	1C8	Sn Ag Cu		96,5 3 0,5	7440-31-5 7440-22-4 7440-50-8	1,8	
Low melting point solder	Heavy Metal	1C8	Sn Bi		42 58	7440-31-5 7440-69-9	1,0	
	Heavy Metal	1C12	Cu		100	7440-50-8	16,2	
Leads Heavy Metal		1C8	Sn		100	7440-31-5	0,5	
The 3rd electrode	Heavy Metal	1A	Fe		100	7439-89-6	4,05	
	Heavy Metal	1C12	Cu		100	7440-50-8	0,70	
	Heavy Metal	1C8	Sn		100	7440-31-5	0,15	
Spring	Heavy Metal	1C	Mn Cr Ni Fe others*)		1,0 16,83 7,09 74,3 0,78	7439-96-5 7440-47-3 7440-02-0 7439-89-6	2,3	
Encapsulation	Duromer	2D Halogen-free coating	SiO2 Epoxy Pyromellitic dianhydride Phosphated epoxy others*)		46 40 6,5 5 2,5	60676-86-0 25036-25-3 89-32-7	6,2	
Housing	Thermoplastic	2A	PBT (Poly butylene terephthalate) Glass fibre Sb2O3 Carbon black Brominated flame retardant Additives		69,5 25 3 0,5 0,5 1,5	26062-94-2 65997-17-3 1309-64-4 1333-86-4	47,6	
		•	•		'	Sum in total:	100	
sizes [mm] 22 X 19.2 35.2 X 30.2	weight range [g] 4.0-5.6 10.0-18.6	material number B72214T* B72225T*	part numbers ETFV14* ETFV25*	sizes [mm] 25.7X 22	weight range [4.8-8.5	g] material num B72220T*	ber part numbers ETFV20K50-K42	20*
Not part of a Pr	oduct Class							



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Contact	Mr. Christoph Ronne	Mr. Christoph Ronner		Important remarks:		
Division	PPD Q QM	PPD Q QM		The declaration li		
Address	8530 Deutschlandsb	8530 Deutschlandsberg, AUSTRIA		product parts, sul otherwise regulat		
	Tel: +43 3462 800 2139	mailto: functional.ppd-eqpm.db@tdk-electronics.tdk.com	2)	This Material Data		
*) others: .(not declarable or prohibited substances acc. GADSL)			based on our be	herein. We expres		
**) typical m	or hinding produc					

- eclaration limit is 0.1% as defined by IEC 62474 (IEC PAS 61906). Traces are ct parts, substances etc. that are below a percentage of 0.1 % by weight, if not vise regulated.
- Material Data Sheet contains typical values of the respective products set forth . We expressly point out that all values and statements contained herein are on our best present knowledge and cannot be regarded as binding statements or binding product specifications, unless otherwise explicitly agreed in writing. TDK ELECTRONICS AG AND ITS AFFILIATES HEREBY EXPRESSLY DISCLAIM ANY REPRESENTATION OR WARRANTY, WHETHER EXPRESS, IMPLIED OR STATUTORY, WITH REGARD TO THE STATEMENTS AND VALUES CONTAINED HEREIN, INCLUDING BUT NOT LIMITED TO ANY REPRESENTATION OR WARRANTY OF MERCHANTABILITY OR SUITABILITY FOR ANY PURPOSE

The products set forth herein are "RoHS-compatible". RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

RoHS - Exemptions for the Product Class / Product according to Annex III: (☑ valid □ not valid)

☑ no exemptions:

☐ Exemption 6 (a): Lead as an alloying element in steel for machining purposes and in galvanized steel containing up to 0,35 % lead by weight;

☐ Exemption 6 (b): Lead as an alloying element in aluminium containing up to 0,4 % lead by weight;

 $\hfill\square$ Exemption 6 (c): Copper alloy containing up to 4 % lead by weight;

□ Exemption 7 (a): Lead in high melting temperature type solder (i.e. lead-based alloys containing 85 % by weight or more lead);

🗆 Exemption 7 (c)-1: Electrical and electronic components containing lead in a glass or ceramic other than dielectric ceramic in capacitors, e.g. piezoelectronic devices, or in a glass or ceramic matrix compound;

☐ Exemption 7 (c)-II: Lead in dielectric ceramic in capacitors for a rated voltage of 125 V AC or 250 V DC or higher;

☐ Exemption 7 (c)-III: Lead in dielectric ceramic in capacitors for a rated voltage of less than 125 V AC or 250 V DC;

🗆 Exemption 15: Lead in solders to complete a viable electrical connection between semiconductor die and carrier within integrated circuit Flip Chip packages;

☐ Other Exemption than above ..