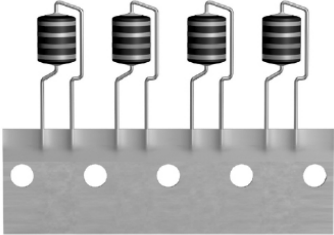


RF Chokes

Material Data Sheet

Product Class:	Large Bobbin Core (LBC) Radial Leads B82144B*****000	
Date	31.10.2016	
IMDS ID if available		
Version:	02	

Product Part (IMDS: semi component)	Material Class (IMDS: Material)	Material (Classification) VDA 231	Substance	TMPS**) [wt-%]	CAS if applicable	typical mass of material [wt-%]	Traces see 1)
Active Part	Ceramic	4B	Nickel Zinc Ferrite	100	12645-50-0	48.6	
	Heavy Metal	1C	Cu	100	7440-50-8	20.6	
	Elastomer	2B	Polyurethane (PUR)	100	68400-67-9	1.0	
Encapsulation and Mounting	Duromer	2C	Epoxy	100	25068-38-6	2.1	
	Organic, solid	5B	Lacquer	100		4.2	
	Heavy Metal	1C	Pb	94	7439-92-1	1.2	
	Heavy metal	1C	Sn	5	7440-31-5		
	Heavy metal	1C	Ag	1	7440-22-4		
Termination **)	Heavy Metal	1C	Cu	99.9	7440-50-8	21.3	
			Ag	0.1	7440-22-4		
	Heavy Metal	1C	Ni	100	7440-02-0		x
	Heavy Metal	1C	Sn	100	7440-31-5	1.0	
Sum in total:						100,0	

Size Ø x L [max. in mm] 6,5 x 11,5 (lacquered)	Weight [approx. in g] 0,95	Part Numbers B82144B*****000
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Not part of a Product Class : ---

<table border="1"> <tr> <td>Contact</td> <td>Dr. Johann Reindl, MAG EPQM</td> </tr> <tr> <td>Division</td> <td>EPCOS AG, Magnetics Business Group (MAG)</td> </tr> <tr> <td>Address</td> <td>Rosenheimer Strasse 116b, 81669 Munich</td> </tr> <tr> <td></td> <td>Tel: +49 89 54020 3030 mailto: johann.reindl@epcos.com</td> </tr> </table>	Contact	Dr. Johann Reindl, MAG EPQM	Division	EPCOS AG, Magnetics Business Group (MAG)	Address	Rosenheimer Strasse 116b, 81669 Munich		Tel: +49 89 54020 3030 mailto: johann.reindl@epcos.com	<p>Important remarks:</p> <p>1) The declaration limit is 0.1% as defined by IEC 62474 (IEC PAS 61906) Traces are product parts, substances etc. that are below a percentage of 0.1 % by weight, if not otherwise regulated</p> <p>2) This Material Data Sheet contains typical values of the respective products set forth herein. We expressly point out that all values and statements contained herein are based on our best present knowledge and cannot be regarded as binding statements or binding product specifications, unless otherwise explicitly agreed in writing. EPCOS AG AND ITS AFFILIATES HEREBY EXPRESSLY DISCLAIM ANY REPRESENTATION OR WARRANTY, WHETHER EXPRESS, IMPLIED OR STATUTORY, WITH REGARD TO THE STATEMENTS AND VALUES CONTAINED HEREIN, INCLUDING BUT NOT LIMITED TO ANY REPRESENTATION OR WARRANTY OF MERCHANTABILITY OR SUITABILITY FOR ANY PURPOSE.</p>
Contact	Dr. Johann Reindl, MAG EPQM								
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<p>*) others: (not declarable or prohibited substances acc. GADSL) **) typical mass percentage of substance ***) lead from high temperature solder can be found on pins in a certain distance from core flange</p>									

The products set forth herein are "RoHS-compatible". RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

RoHS - Exemptions for the product class / product according to Annex III: (valid not valid)

- no exemptions;
- Exemption 6 (a): Lead as an alloying element in steel for machining purposes and in galvanized steel containing up to 0,35 % lead by weight;
- Exemption 6 (b): Lead as an alloying element in aluminium containing up to 0,4 % lead by weight;
- Exemption 6 (c): Copper alloy containing up to 4 % lead by weight;
- Exemption 7 (a): Lead in high melting temperature type solder (i.e. lead-based alloys containing 85 % by weight or more lead);
- Exemption 7 (c)-I: Electrical and electronic components containing lead in a glass or ceramic other than dielectric ceramic in capacitors, e.g. piezoelectronic devices, or in a glass or ceramic matrix compound;
- Exemption 7 (c)-II: Lead in dielectric ceramic in capacitors for a rated voltage of 125 V AC or 250 V DC or higher;
- Exemption 7 (c)-III: Lead in dielectric ceramic in capacitors for a rated voltage of less than 125 V AC or 250 V DC;
- Exemption 15: Lead in solders to complete a viable electrical connection between semiconductor die and carrier within integrated circuit Flip Chip packages;
- Other Exemption than above