

Ceramic transient voltage suppressors, CTVS

Reliability

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1 Overview

Reliability is defined as the ability of a component to perform its intended (specified) function without failure in a given period of time (the lifetime of the component) under stated conditions. In mathematically terms, reliability is also expressed as the probability that a component will fail to function after a specified time interval.

The reliability of ceramic transient voltage suppressors is dependent on their design, material properties, manufacturing process and life cycle environment.

Failure rate

Information on component failure rates provides the manufacturer with a basis for reliability forecasts and allow them to estimate future service requirements.

If the fraction ΔN of a large number N of identical components fails during the time Δt , the failure rate (averaged over Δt) is indicated by $\lambda = \Delta N / (N \cdot \Delta t)$. The failure rate depends on the failure criteria, the load and the operating time.

The dimension of the failure rate is the reciprocal of time and the unit used is $10^{.9}$ / h = 1 fit (failure in time).





Region II is assumed to be the "service period" of components. It is thus considered to be sufficient to state the (virtually) constant failure rate λ_0 .

1.1 Lifetime

The mean life of CTVS devices as a function of

- voltage class
- ambient temperature
- applied voltage ratio (AVR)

can be derived from figure 2.



The applied voltage ratio AVR is defined as the ratio between intended operating voltage and maximum permissible operating voltage

$$AVR = \frac{V^*}{V_{max}}$$
 (equ. 1)

Reaching the maximum average power dissipation is defined as the end of useful life. But the CTVS is still functional.

The increase in leakage current is, to a good approximation

$$i_{l} = A + k \cdot \sqrt{t}$$
 (equ. 2)

iL leakage current at constant voltage

A factor, dependent on temperature, AVR, geometry, encapsulating material

k slope coefficient of leakage current over \sqrt{t}

Investigations at different temperatures and AVRs show that the logarithm of lifetime is in a linear relation to reciprocal ambient temperature. The slope of this curve is virtually constant for zinc oxide. It can be attributed to activation energy.

The theoretical background of these relations is known as the Arrhenius model. Figure 2 shows evaluation for CTVS components.

EPCOS lifetime tests extend over a period of several 10³ hours. The higher lifetime figures are determined by extrapolation on the Arrhenius model.

1.2 Failure rate

The failure rate λ is the reciprocal of mean life in hours, the unit being fit (failures in time) = 10⁻⁹/h.

$$\lambda = \frac{10^9}{ML[h]} \text{ [fit]}$$
 (equ. 3)

Accordingly, typical failure rates can be derived from mean life figures dependent on applied voltage and temperature or schematically shown in figure 2.



Figure 2 Mean life on Arrhenius model for CTVS. Applied voltage ratio (AVR) referred to maximum permissible operating voltage



Failure rate figures refer to the average production status and are therefore to be understood as mean values (statistical expectations) for a large number of delivery lots of identical CTVS devices. These figures are based on application experience and on data obtained from preceding tests under normal conditions, or – for purposes of accelerated aging – more severe conditions.

2 Reliability tests

A variety of endurance tests and environmental tests are conducted to assure the reliability of ceramic transient voltage suppressors. These tests are derived from the extremes of expected application conditions, with test conditions intensified to obtain authoritative results within a reasonable period.

The reliability testing programs of EPCOS are based on the test plans of international standards and customer requirements.

EPCOS performs reliability tests to qualify new component families and for periodic requalification.

2.1 Tests of multilayer varistors (MLVs) and CeraDiodes

The following tests for MLVs and CeraDiodes are performed according to IEC 60068. Preconditioning: reflow soldering on PCB.

After testing a visual check is performed. Criteria: no visible damage of component.

Test	Standard	Test method/conditions	Criteria
Surge current derating, 8/20 μs	IEC 61000-4-5	Surge current (8/20 μs), unipolar, amplitude corresponding to derating curve for 20 μs Number of pulses: 1	$ \Delta V/V (1 \text{ mA}) \le 10\%$ (measured in direction of surge current)
ESD contact discharge	IEC 61000-4-2	Test voltage: up to 8 kV Number of test pulses: 20 Polarity: +/– Discharge network: 150 pF, 330 Ω	ΔV/V (1 mA) depending on type
ESD air discharge	IEC 61000-4-2	Test voltage: up to 15 kV Number of test pulses: 20 Polarity: +/– Discharge network: 150 pF, 330 Ω	∆V/V (1 mA) depending on type
Load dump (only for specific automotive types)	ISO 16750-2	Number of pulses: 10 Pulse interval: 60 s	$ \Delta V/V (1 \text{ mA}) < 15\%$ (measured in direction of load)

Pulse strength tests



Environmental tests

Test	Standard	Test method/conditions	Criteria
High-temperature life test	MIL-STD-202F, method 108A, condition D	Temperature: T _{op,max} Duration: 1000 h Applied voltage: V _{DC,max}	∆V/V (1 mA) ≤ 10%
Fast temperature cycling (thermal shock)	IEC 60068-2-14, test N _a	Minimum temperature: LCT Maximum temperature: UCT Dwell time: 15 min Transfer time: < 10 s Number of cycles: depending on type	∆V/V (1 mA) ≤ 10%
Damp heat, steady state	IEC 60068-2-78	Temperature: 40 °C Humidity: 93 % r.h. Duration: 56 days Applied voltage: 0.1 · V _{DC,max}	∆V/V (1 mA) ≤ 10%
Biased humidity (only for specific automotive types)	IEC 60068-2-67 Cy	Temperature: 85 °C Humidity: 85 % r.h. Duration: 1000 h Applied voltage: V _{DC,max}	∆V/V (1 mA) ≤ 10%
High-temperature exposure	IEC 60068-2-2 Ba	Temperature: UCT Duration: 1000 h Applied voltage: unpowered	$ \Delta V/V (1 \text{ mA}) \le 10\%$



Mechanical strength tests

Test	Standard	Test method/conditions	Criteria
Bump/mechanical shock	IEC 60068-2-27	Pulse duration: 6 ms Max. acceleration: 400 m/s ² Number of bumps per direction: 5000 Directions: 6 Pulse: half sine	ΔV/V (1 mA) ≤ 5%
Vibration	IEC 60068-2-6 Fc	Frequency range: 10 55 Hz Amplitude: 0.75 mm or 100 m/s ² Duration: 6 hrs $(3 \cdot 2 h)$ Pulse: sinewave	∆V/V (1 mA) ≤ 5%
Board flex (For case sizes ≥ 0603 only)	IEC 60068-2-21 U _{e1}	Deflection: 2 mm Duration: 60 s	$ \Delta V/V (1 \text{ mA}) \le 10\%$ $ \Delta C/C_0 \le 10\%$
Terminal strength	IEC 60068-2-21 U _{e3}	Shear force applied to component soldered on PCB	Shear force: depending on case size ≥ 2 N for chip 0201 ≥ 5 N for chip 0402 ≥ 10 N for chip 0603 2220 ≥ 5 N for array components

Note for automotive application:

Especially for automotive applications EPCOS performs qualification based on AEC-Q200, Rev. D. In term of PPAP request test data of representatives of product family will be delivered.



2.2 Tests of ESD/EMI filters

The following tests for ESD/EMI filters are performed according to IEC 60068. Preconditioning: reflow soldering on PCB.

After testing a visual check is performed. Criteria: no visible damage of component.

Pulse strength tests

Test	Standard	Test method/conditions	Criteria
ESD contact discharge	IEC 61000-4-2	Test voltage: up to 8 kV Number of test pulses: 20 Polarity: +/- Discharge network: 150 pF, 330 Ω	∆V/V (1 mA) depending on type
ESD air discharge	IEC 61000-4-2	Test voltage: up to 15 kV Number of test pulses: 20 Polarity: +/- Discharge network: 150 pF, 330 Ω	∆V/V (1 mA) depending on type

Environmental tests

Test	Standard	Test method/conditions	Criteria
High-temperature life test	MIL-STD-202F, method 108A, condition D	Temperature: T _{op,max} Duration: 1000 h Applied voltage: V _{DC,max}	$ \Delta V/V (1 \text{ mA}) \le 10\%$
Fast temperature cycling (thermal shock)	IEC 60068-2-14, test N _a	Minimum temperature: LCT Maximum temperature: UCT Dwell time: 15 min Transfer time: < 10 s Number of cycles: depending on type	ΔV/V (1 mA) ≤ 10%
Damp heat, steady state	IEC 60068-2-78	Temperature: 40 °C Humidity: 93 % r.h. Duration: 56 days Applied voltage: 0.1 ·V _{DC,max}	∆V/V (1 mA) ≤ 10%



Mechanical strength tests

Test	Standard	Test method/conditions	Criteria
Bord flex (For case sizes ≥ 0603 only)	IEC 60068-2-21 U _{e1}	Deflection: 2 mm Duration: 60 s	$\begin{split} \Delta V/V ~(1~\text{mA}) &\leq 10\% \\ \Delta C/C_0 &\leq 10\% \end{split}$
Terminal strength	IEC 60068-2-21 U _{e3}	Shear force applied to component soldered on PCB	Shear force: \geq 10 N

2.3 Tests of leaded transient voltage/RFI suppressors

The following tests for leaded varistors (SHCV) are performed according to IEC 60068. After testing a visual check is performed. Criteria: no visible damage of component.

Pulse strength tests

Test	Standard	Test method/conditions	Criteria
Surge current derating, 8/20 μs	IEC 61000-4-5	Surge current (8/20 μs), unipolar, amplitude corresponding to derating curve for 20 μs Number of pulses: 1	$ \Delta V/V (1 \text{ mA}) \le 10\%$ (measured in direction of surge current)
Load dump (only for specific automotive types)	ISO 16750-2	Number of pulses: 10 Pulse interval: 60 s	$ \Delta V/V (1 \text{ mA}) < 15\%$ (measured in direction of load)

Environmental tests

Test	Standard	Test method/conditions	Criteria
High-temperature life test	MIL-STD-202F, method 108A, condition D	Temperature: T _{op,max} Duration: 1000 h Applied voltage: V _{DC,max}	$\begin{split} \Delta V/V ~(1~mA) &\leq 10\% \\ \Delta C/C_0 &\leq 10\% \end{split}$
Fast temperature cycling (thermal shock)	IEC 60068-2-14, test N _a	Minimum temperature: LCT Maximum temperature: UCT Dwell time: 15 min Transfer time: < 10 s Number of cycles: depending on type	$ \Delta V/V (1 \text{ mA}) \le 10\%$ $ \Delta C/C_0 \le 10\%$
Damp heat, steady state	IEC 60068-2-78	Temperature: 40 °C Humidity: 93 % r.h. Duration: 56 days Applied voltage: 0.1 · V _{DC,max}	$ \Delta V/V (1 \text{ mA}) \le 10\%$ $ \Delta C/C_0 \le 10\%$
High-temperature exposure	IEC 60068-2-2 Ba	Temperature: UCT Duration: 1000 h Applied voltage: unpowered	$ \Delta V/V (1 \text{ mA}) \le 10\%$ $ \Delta C/C_0 \le 10\%$



Mechanical strength tests

Test	Standard	Test method/conditions	Criteria
Bump/mechanical shock	IEC 60068-2-27	Pulse duration: 6 ms Max. acceleration: 400 m/s ² Number of bumps per direction: 5000 Directions: 6 Pulse: half sine	$ \Delta V/V (1 \text{ mA}) \le 5\%$ $ \Delta C/C_0 \le 5\%$
Vibration	IEC 60068-2-6 Fc	Frequency range: 10 55 Hz Amplitude: 0.75 mm or 100 m/s ² Duration: 6 h ($3 \cdot 2$ h) Pulse: sinewave	$ \Delta V/V (1 \text{ mA}) \le 5\%$ $ \Delta C/C_0 \le 5\%$
Terminal strength	IEC 60068-2-21 U _{a1}	Force applied to the terminal in direction of its axis	$\begin{split} \Delta V/V \ (1 \ mA) &\leq 5\% \\ \Delta C/C_0 &\leq 5\% \\ No \ break \ of \ solder \\ joint, \ no \ wire \ break \end{split}$

Note for automotive application:

Especially for automotive applications EPCOS performs qualification based on AEC-Q200, Rev. D. In term of PPAP request test data of representatives of product family will be delivered.